

Figure 1. Detailed Block Diagram of Preferred Method for Frequency Hopping

DIGITAL - TO - ANALOG CONVERTER 2

SYSTEM MICROPROCESSOR

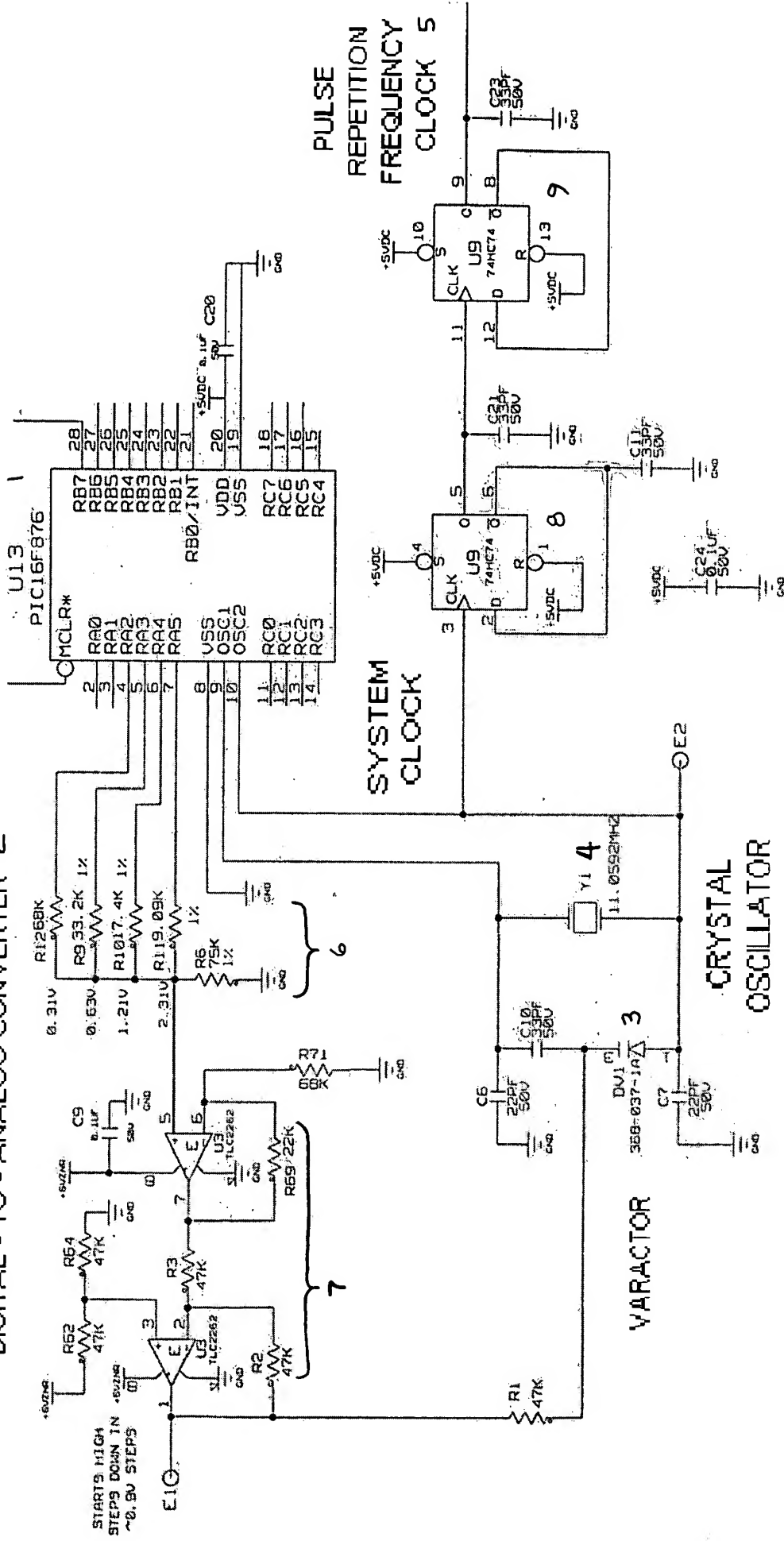


FIGURE 2

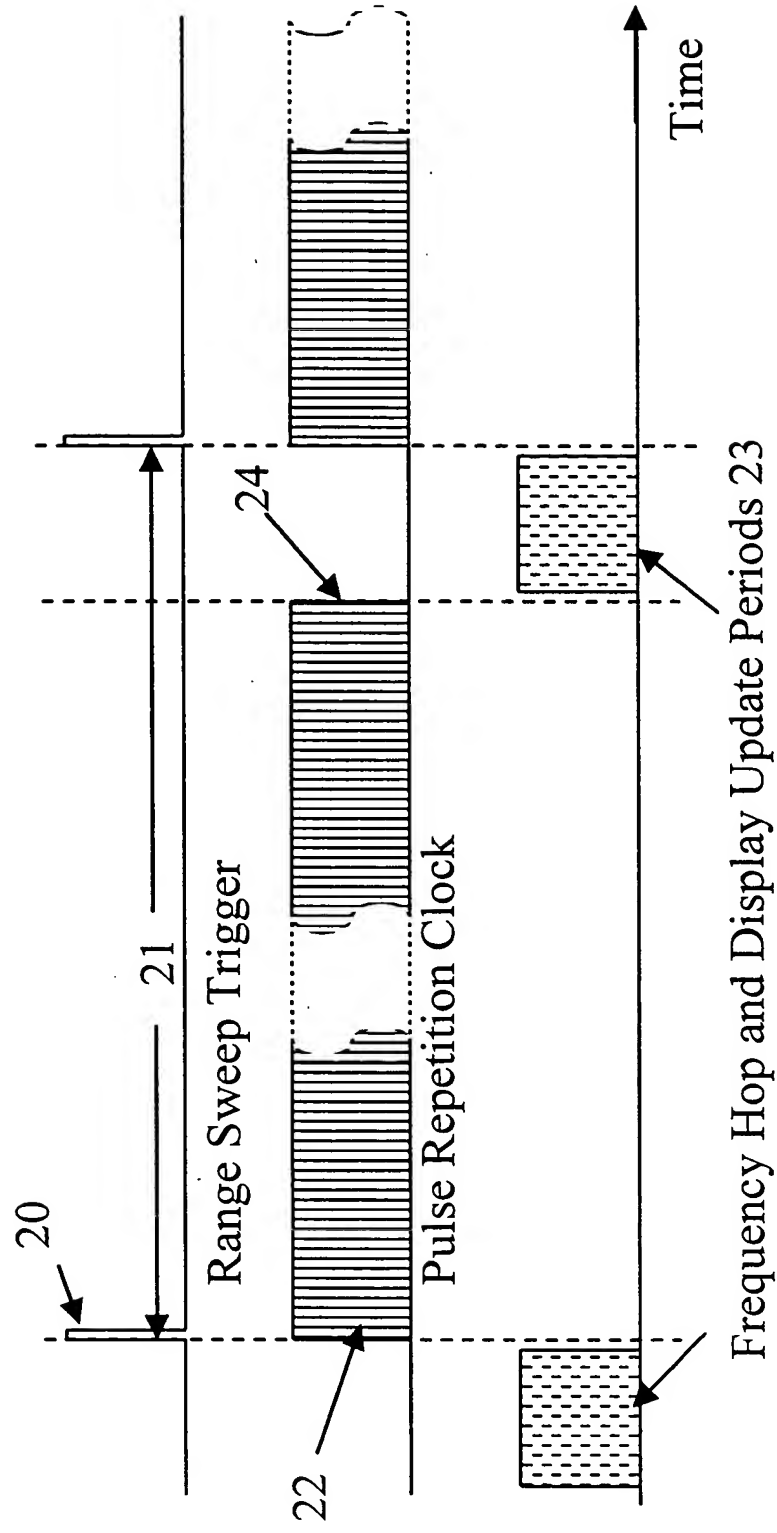


Figure 3. Timing Diagram of One Preferred Method for Frequency Hopping